To:

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Kevin M. Harer et al.

Appl. No. 09/760,063

Filed: Jan. 12, 2001

For:

SIMULATION-BASED FUNCTIONAL VERIFICATION OF MICROCIRCUIT

**DESIGNS** 

Conf. No. 2596

Art Unit: 2123

Examiner: T. Stevens

Atty. Docket: 06816.0173

Cust. No. 35795

## **Information Disclosure Statement**

Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

Sir:

The Examiner is requested to consider the documents listed on accompanying Form PTO/SB/08B and make this information of official record in the application.

Applicants reserve the right to prove that this information may not be enabling for the teachings purportedly offered.

Applicants have checked the appropriate boxes below.

- □ 1. This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No statement under 37 C.F.R. § 1.97(e) or fee is required, or
- ☑ 2. This Information Disclosure Statement is being filed more than three months after
  the U.S. filing date AND after the mailing date of the first Office Action on
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Applicant Harer Appl. No. 09/760,063 IDS, Page 2 of 3

patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. 37 C.F.R. § 1.97(e)(1), or

- Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to my knowledge after making reasonable inquiry, no item of information contained in this Information Disclosure Statement was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing of this Information Disclosure Statement. 37 C.F.R. § 1.97(e)(2), or
- ☑c. Attached is our Credit Card Payment form to cover the payment of the
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The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 502584 referencing docket number 06816.0158.

Applicant Harer Appl. No. 09/760,063 IDS, Page 3 of 3

Respectfully submitted,

Jonathan I. Kaplan

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Date: May 27, 2005 JONATHAN T. KAPLAN ATTORNEY AT LAW 140 NASSAU STREET NEW YORK, NY 10038-1501 TEL. 917-674-5017

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STATEMENT BY APPLICANT			PPLICANT	First Named Inventor	Kevin Harer .	
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		M. Kantrowitz and L. Noack. I'm Done Simulating; Now what? Verification Coverage Analysis and Correctness Checking of the DECchip 21164 Alpha microprocessor. Proceedings of 33rd DAC, 1996. pp. 325-330.	

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